

*“ Everything's numbers go down far enough, don't you know ? ”*

*She poked him, punched his arms .*

*“ You think this is continuous ? You think there's anything but math ? ”*

*He knew there wasn't ... digital physics had reigned supreme since before he'd been born and its dictums were as incontrovertible as they were absurd .*

*Numbers didn't just **describe** reality ; numbers **were** reality, discrete step functions smoothing up across the planck length into an illusion of substance .*

**ECHOPRAXIA**

[ MODULE ONE : FUNDAMENTAL KNOWLEDGE SET ]

--	--
00 : M1E	<i>epigraph</i>
00 : M1I <	<i>index</i>
--	--
01 : NXS	<i>NEXUS / 3C min-fi network</i>
02 : LUM	<i>Light unit metric ( virtual fiat )</i>
03 : MDE	<i>MICRO-BAND digital electricity</i>
04 : SSL	<i>solid state logic / LOGIQUE</i>
05 : QNT	<i>QUINT ( tibetan board game )</i>
06 : SCD	<i>spatial computing / AR demo</i>
07 : SSA	<i>solid state architecture</i>
08 : LSV	<i>Large sum validation algorithm</i>
09 : LVA	<i>LEVIATHAN virtual array</i>
10 : OCS	<i>overall cryptographic scope</i>
11 : EQS	<i>essential quantum set</i>
12 : DPL	<i>data penetration layers</i>
13 : QTE	<i>QUANTA : timing &amp; electric</i>
14 : HEX	<i>REX consignment / recovery</i>
15 : TAO	<i>NON-DUALITY : summation</i>
16 : NPS	<i>n-POINT set / seal</i>



mu:BECKON - 2022 : NEXUS BROADBAND FOUNDER / XHDL

### LETTERS FROM TIBET - Q3 2024

what binds us together ? In a word, electricity and its application .

This with mounting tension and hostility, however, as global leaders clamour to find common solution for what has grown unmanageable before complacent eyes : corruption, poverty, and greed .

Nevertheless, TIBETAN input on these difficult matters is brief : *please hear us out* .

A

### BHUMI NONE & THE UNLIKELY LUM

Historically, the tale of the LUM ( LIGHT UNIT METRIC ) begins in TIBET and ends in CANADA some 160 years later as a feat of qualified quantum mechanics ; naturally, this unlikely development invites some degree of explanation .

Thankfully, the tables found below help guide our understanding through to the central insight of mysticism and mathematics alike : BHUMI 0 .

1 - KASINA	2 - NOR 2 OR	3 - n.ORIGIN
• •	A - 1 . 0625	A - 10 . 000
• •	B - 1 . 0000	B - 2 . 5000
• •	C - 0 . 0625	C - 2 . 2500
• •	D - 10 . 625	D - 2 . 1250
•	E - 10 : 625	E - X : 0###

C

### NEXUS MIN-FI : INTERNET \_REBOOT

Access to NEXUS BROADBAND is facilitated by way of a 1-W steady-rate 16-QIT circuit dubbed CRICKET -- which, in networked unison, cooperates and competes with other iterations of itself to perform complex cryptographic feats -- and, in return, yield borderless digital fiat ( X-CASH / USX ) to the participant .

CRICKET itself is quantized to a one hertz MIN-FI network refresh rate wherein mere participation affords participants a *guaranteed* baseline return of 125 USX PM -- for labour that CRICKET has performed on your behalf !

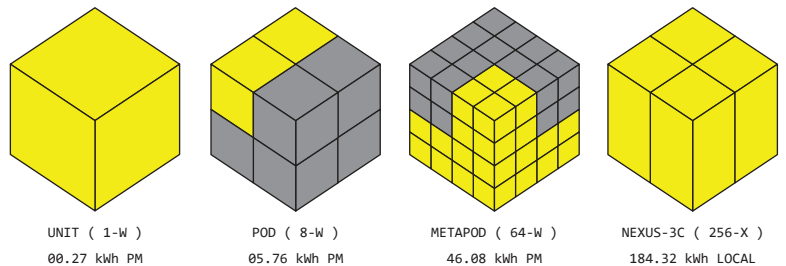
In fact, on our network the electrical grid itself is monitored to ensure that the effective HASHRATE of our virtual centers can *never* excel a sensible live limit relative to the region who sponsors the electricity / hosting .

Speaking of servers and their administration, the TIBETAN LUM was minted under a strict OPEN SOURCE policy and all schematics relevant to our network and its ongoing deployment may be freely interrogated by the public .

If you are interested in learning how the TIBETAN LUM can enrich your business in addition to your person, please inquire with one of our authorized representatives to discuss the ESR ( ECOLOGICAL SAFETY RECORD ) program : a free-accord, grass-roots, GREEN CITIZEN social initiative .

Find us online : [black-lotus-society.ca](http://black-lotus-society.ca)

#### 1 - DEFAULT HOSTING TIERS - CRICKET / POD / META-POD / NEXUS :



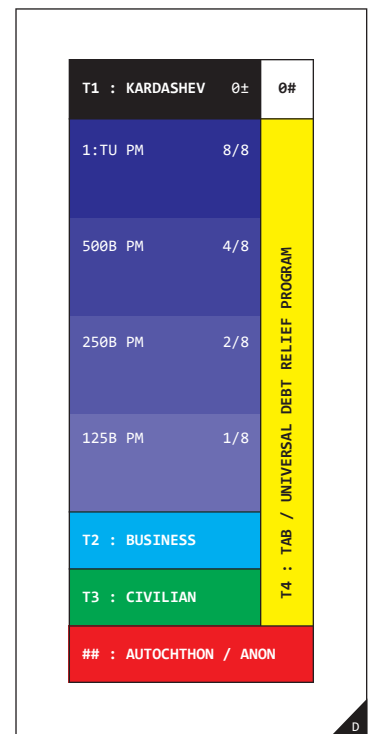
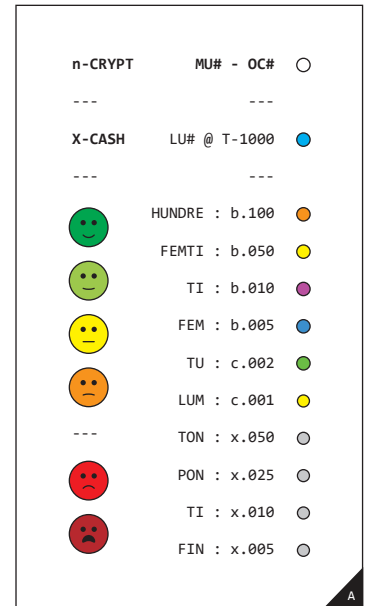
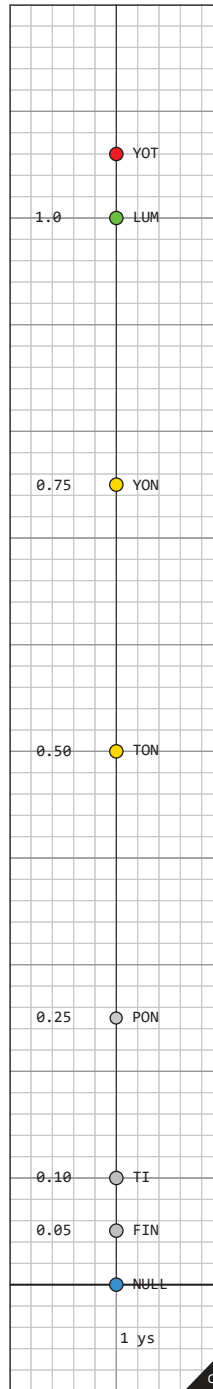
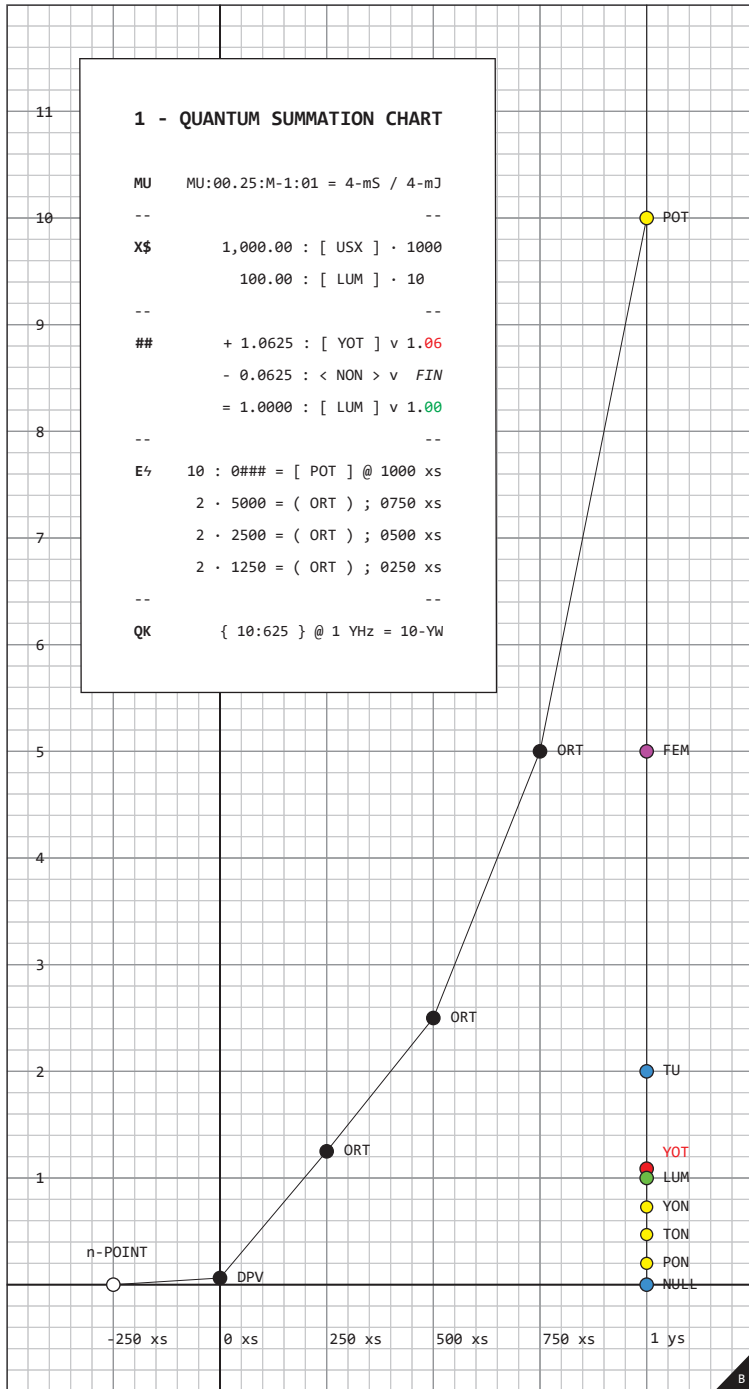
B

### BLADE-XPB : EXCLUSIVE PRIVATE NETWORK

Whilst our hardware engineers work to bring CRICKET to market, our software engineers are also busily preparing for consumer launch ( 2026 ) with our premiere software : BLADE-XPB .

BLADE-XPB is a free, open-source VPN ( VERY PRIVATE NETWORK ) protocol which doubles as a dedicated authentication service / firewall for your entire NEXUS portfolio -- private no-fee banking / X-CASH wallet ( MACRO ) included .

D



1 - MACRO STRUCTURE

##-##

--

##-A1 | ##-A2 | ##-A3

##-B1 | ##-B2 | ##-B3

##-C1 | ##-C2 | ##-C3

##-D1 | ##-## | ##-##

2 - UNLOCK / MAXIMUM

MU# - SU#

--

100K - 1M USX

10K - 100K USX

1K - 10K USX

FREE - 5K USX

3 - TIER / APPLICATION

T0 : ENTERPRISE

--

T1 : ENTREPRENEUR

T2 : BUSINESS

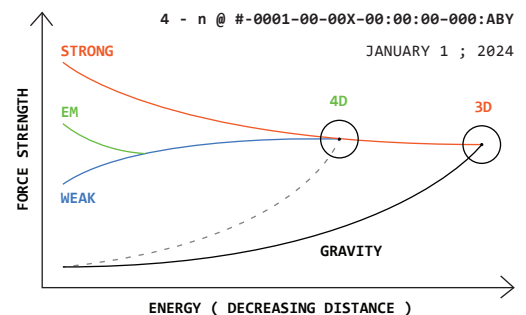
T3 : CIVILIAN

T4 : AUTOCHTHON

√ RESTRICTIONS : ANY NUMERAL ( 1-9 ) or LETTER ( A-Z ; UPPER-CASE ) + SYMBOL ( EX : 1# )

√ UP TO 1000 USX PM / 125 BASELINE PEGGED @ 1:1 SOLIDARITY WITH USD IN PERPETUITY ( TAB )

√ UDRP : REGIONAL CURRENCY BUYOUT / FORFEIT & LONG-TERM HOLDINGS / FINANCIAL INSURANCE



# MICRO-BAND DIGITAL ELECTRICITY ( QUANTA )

QUANTA refer to discrete packets of energy that are quantized, meaning they exist in preset amounts associated with specific electromagnetic properties and tasks . In essence, QUANTA represent the smallest possible units of energy that can be applied to the paradigm of computer science in order to impel change in state .



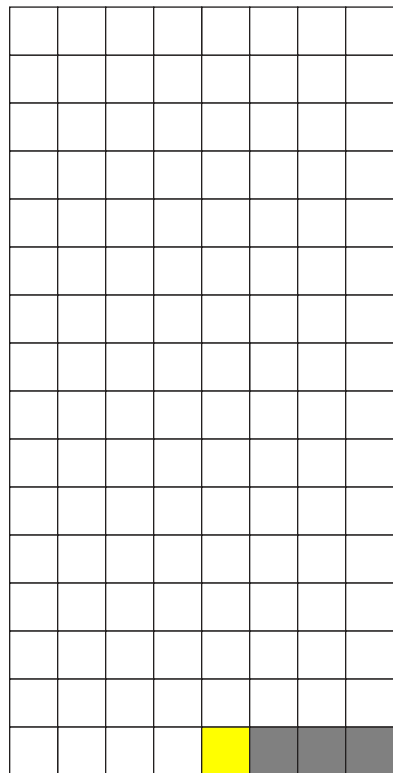
**1 - NEXUS BROADBAND ( QBLI-3C )**  
MIN-FI 5G NETWORK ALTERNATIVE  
*PARALLEL band*

**2 - PARALLEL ENERGY STANDARD ( E.10 / E:10 )**

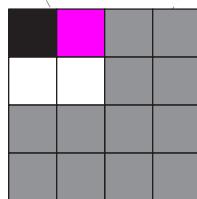
COMPATIBLE W/ MOST INDUSTRY CERTIFICATES  
*PARALLEL band*

**3 - CRICKET ( USB-X )**  
NETWORK ACCESS DEVICE  
*PARALLEL band*

- X - CTAN . NHI  
NON-HUMAN INTELLIGENCE  
*EXTRA-PERCEPTUAL band ( 4D+ )*
- I - HOLO . SIM  
SENTIENT ELECTRONIC LIFE FORM  
*PERCEPTUAL band ( 3D )*



**5 - POSITRONICS TRAY ( MOD-125 )**  
e.005 per cell @ 1000^1 / 1 kHz  
*POSITRONICS band*



**4 - QYTE ( LOGIQUE-16 )**  
8:80 / POD @ 1-8 Hz LATCH  
*PERCEPTUAL band*

NOR.1			NOT.1
0R.2-3			xAND.3

**8 - AI-X HYPER-KERNEL ( HEX\_8 )**  
e.008 MAX @ 1000^X-4 / 250 xs  
*V-PLANCK band*

#	SUM_#		5	LED = WHITE			
1	MOD_R			255 : RED			
2	MOD_G			255 : GREEN			
2	MOD_B			255 : BLUE			
5	RGB_5	<table border="1"><tr><td>R</td><td>G</td><td>B</td></tr></table>	R	G	B		0# : GLYPH
R	G	B					

**6 - LED MICRO-MODULE ( RGB\_5 )**  
e.001 - e.005 ( FLOOR / CEILING )  
*POSITRONICS band*

INPUT		OUTPUT
A	B	Q
1	0	1
0	1	1
-	-	-
0	0	X

**7 - CENTRAL PING ( X:TONE )**  
p.001 - 2 @ 1-4 Hz  
*TRANSIT band*

**CHESS / COURT**

**QIT / AI-Q**

R:QOR

r.xor : 1 / 0  
r.xnor : 2 / N  
r.xand : 1 + 1

- 1 - MASTER QIT ENVELOPE ; MICRO-BAND ELECTRICITY ( E:10 )
- OUTCOME ( CHESS ) : ON EXISTS
- OUTCOME ( QUINT ) : SOVEREIGN @ A4 ; TAKE : 20

**GO ( 5x5 )**

**LOGIQUE / AI-S**

nor.1	xnor.2	not.1	xor.0
xnor.2	xnor.2	xor.0	xor.0
or.2-3	xor.0	qand.1-3	and.3
xor.0	xor.0	and.3	and.3

- 2 - XPU CORE ; AI-S ( SELF or SENTIENT ELECTRONIC LIFE FORM )
- OUTCOME 1 ( GO ) : GRID EXISTS
- OUTCOME 2 ( COURT ) : SINGLE GOVERNANCE STRING

**SCRABBLE / TILES**

**XIT / AI-X**

<b>A</b>	<b>B</b>
NOR-1	NOT-0
<b>Q</b>	<b>#</b>
0R-0	xAND-125

- 3 - NIBBLE SECTOR ; AI-X ( MACHINE-LAYER AI INTEGRATION )
- OUTCOME ( SCRABBLE ) : TICK ( PROPER NOUN / OFFENSIVE )
- OUTCOME ( TILES ) : NOR † OUGHT

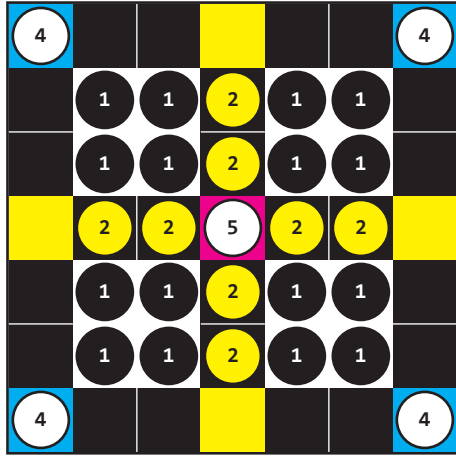
**TIC-TAC-TOE / CRUMB**

**BLADE-TLX / AI-G**

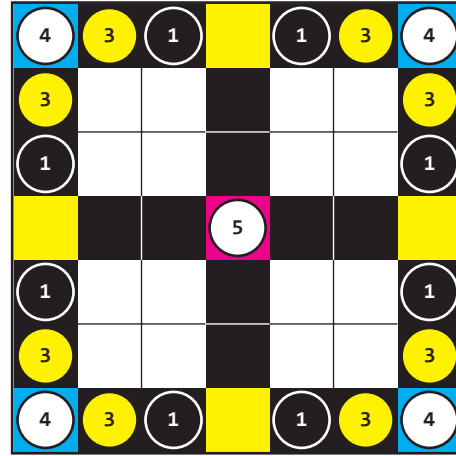
<b>1</b>	2	3
4	5	6
7	8	9

- 4 - UNIVERSAL LOGIN ; AI-G ( VIRTUAL ELECTRONICS / ROBOTICS )
- OUTCOME ( TIC-TAC-TOE ) : Z DOES NOT EXIST .
- OUTCOME ( CRUMB ) : X TAKES 0 .

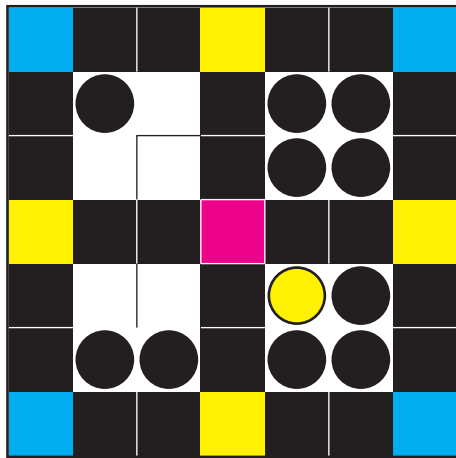
[ QUINT ]



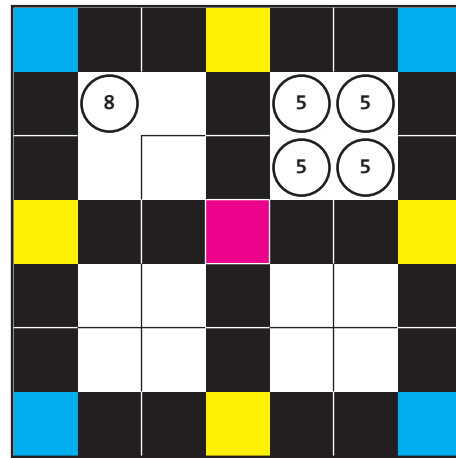
1 - STORED



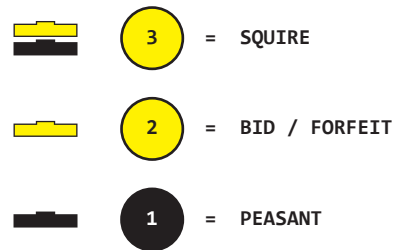
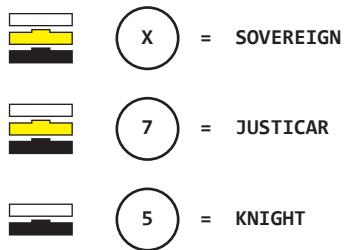
2 - STAGED



3 - CONTEMPLATION



4 - FINALE : SOVEREIGN @ 1A ; TAKE : 20



R	:	1	3	5	7	8
K	-	0	0	4	4	5
B	-	0	2	0	2	2
P	:	1	1	1	1	1

# [ NULL-POINT / PAGODA FOLD ]

8.5 x 11 in / 612 px x 792 px ( DIMENSIONS )

1 ( DIVISIONS ) / ANSI ( TECHNICAL DEFINITION / GROUP )

THEME IN BRIEF : " OVER A PERIOD OF JUST OVER TWO YEARS, A THEN-CANADIAN & SELF-INITIATE INTO TIBETAN REVIVALIST THEOLOGY ( TERTÖN / MENTAT )  
 MANAGED TO RE-ENGINEER BLOCKCHAIN TECHNOLOGIES ON A NEW 16-QIT OR " QYTE " STANDARD SO AS TO PROVE THAT THE MODULAR TECHNOLOGY ITSELF COULD BE  
 SUCCESSFULLY REDEDICATED TO THE PLANET IN THE COOPERATIVE EFFORT OF A STANDARDIZED " WORLD DOLLAR " ( LUM / UDRP ) FOR THE EXPRESS PURPOSE OF RELIEVING  
 INFLATION, BANKRUPTCY, WORLD-CRIPPLING DEBT, AND SPIRITUAL PERSECUTION RELATED TO OUT-OF-SCOPE BINARY EXCESS -- NOW ; WOULD YOU CARE TO SEE THE DEMO ?

**10 - DIGITAL DUAL CITIZENSHIP**

3.5 x 2.0 in / 252 x 144 px

125-TAB PM / BUSINESS CARD

**11 - NIMITTA / KASINA**

0.007 in<sup>2</sup> / 0.5 px

4 / VNM-0X

**12 - VIRTUAL OBJECT**

4.25 in<sup>3</sup> v 4 in<sup>3</sup>

1 / CUBE

**09 - ARC LANGUAGE LIBRARY**

PROJECT SIZE : 100 MB

AI-G CENTRAL DATABASE

**08 - BLADE-XPN / TLX**

1.4142 in / 101.8234 px

E-6:3639 / PINION-##

**07 - IXO / ORIGIN**

10<sup>^</sup>25-YW / 0.0625 px<sup>2</sup>

n-DIMENSIONAL / 10:625

**01 - n-POINT FOLD**

1 in<sup>2</sup> / 72 px<sup>2</sup>

2 / TRANCE

**02 - SCHEMATIC ONE**

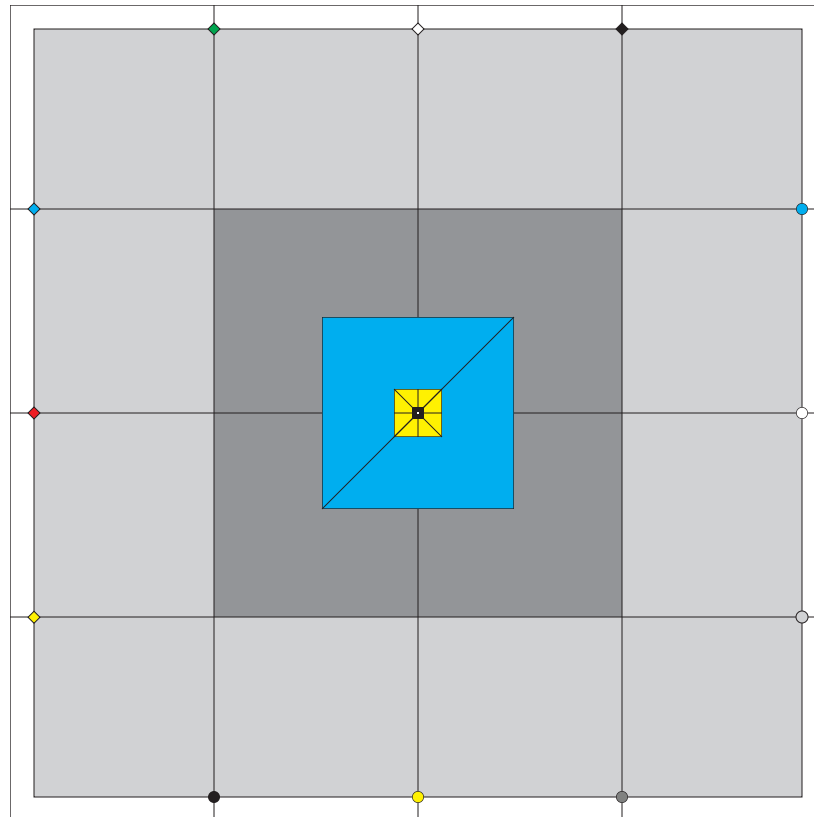
1.0625 in<sup>2</sup> / 76.5 px<sup>2</sup>

8 / BINARY-64

**03 - SCHEMATIC TWO**

4 in<sup>2</sup> / 288 px<sup>2</sup>

16 / TRINARY-128



**06 - CRICKET**

0.0625 in<sup>2</sup> / 4.5 px<sup>2</sup>

1 / QIT

**05 - DEFAULT GROUP**

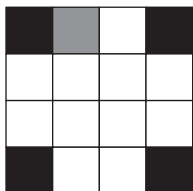
0.25 in<sup>2</sup> / 18 px<sup>2</sup>

8 / POD

**04 - NEXUS SERVER GRID**

2.125 in<sup>2</sup> / 153 px<sup>2</sup>

4 / TITAN-1024



## LIGHT KASINA : ORIGAMI-AR INSTRUCTIONS

First, fold page in half along X and Y page axes and trim along ORIGAMI border .

Next, repeat the same steps, then use the remaining guides to achieve 16 sub-divisions .

Finally, pinch and pierce center carefully and view before a window or other backlit surface .

( compatible AR glasses are required to view VIRTUAL / 4D elements .

## LEGEND

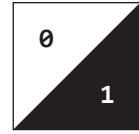
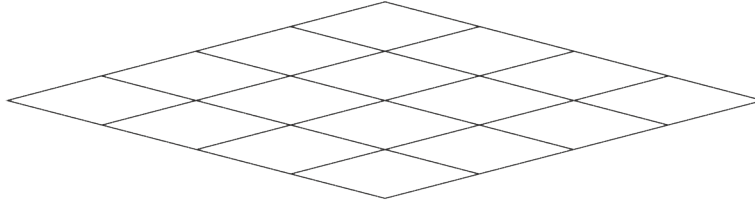
● : ORIGAMI ELEMENT

◆ : VIRTUAL ELEMENT

[ EXCLUSIVE GATES : 128-NIT / UTF-X ]

**QIT / QYTE**

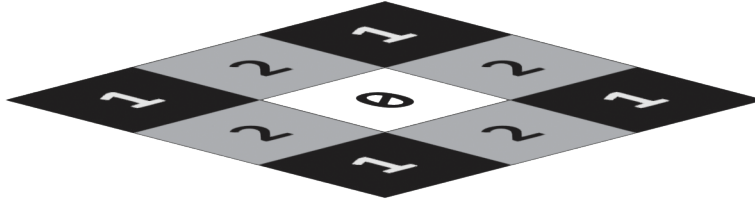
ensures EITHER  
XOR ( 16 )



0 / 1A v 1B

**NIT / NYTE**

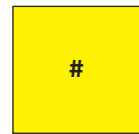
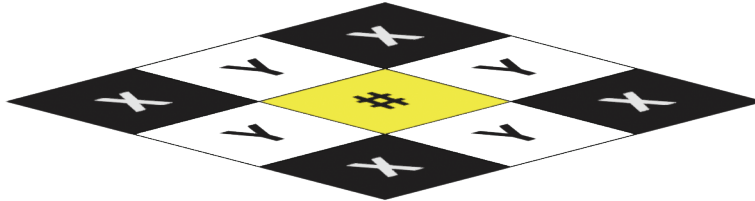
ensures NEITHER  
XNOR ( 0 - 16 )



2 / NULL

**XIT / XYTE**

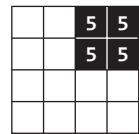
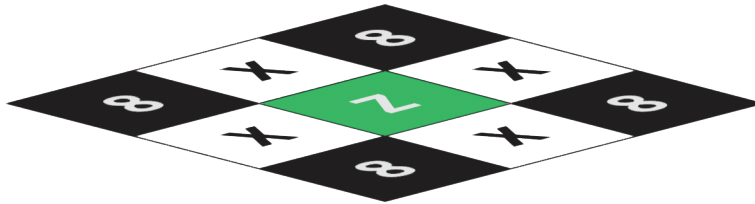
ensures NAND  
XAND ( 4 - 1024 )



4 - 9 / X

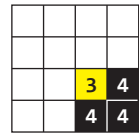
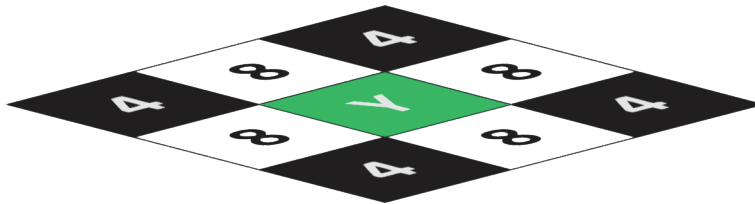
[ PARALLEL GATES : 64-BIT / UTF-8 ]

NOT ( 1 )  
not ( 4 )



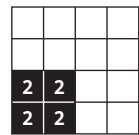
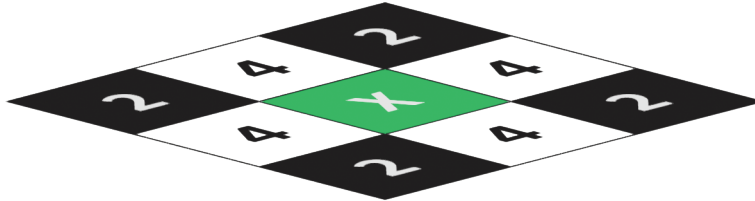
SECTOR 5 / 16-b

nAND ( 1 )  
--  
nand ( 1 )  
and ( 3 )



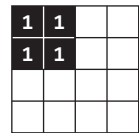
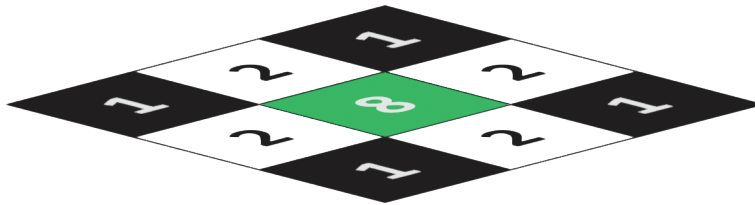
SEC 3 + 4 / 12-x

OR ( 1 )  
or ( 4 )



SECTOR 2 / 08-b

NOR ( 1 )  
nor ( 4 )



SECTOR 1 / 04-b

[ 1:LU# ] : THOUSAND : K / 01 / 03	[ 1:SU# ] : SEXTILLION : Z / 07 / 21	[ 0:DD# ] : DUODECILLIO- : # / 13 / 39	[ 0:OD! ] : OCTODECIL- : # / 19 / 57
[ 1:MU# ] : MILLION : M / 02 / 06	[ 1:YU# ] : SEPTILLION : Y / 08 / 24	[ 0:TD# ] : TREDECILLION : # / 14 / 42	[ 0:ND! ] : NOVEMDECI- : # / 20 / 60
[ 1:BU# ] : BILLION : G / 03 / 09	[ 1:OC# ] : OCTILLION : O / 09 / 27	[ 0:QD! ] : QUATTUORDEC- : # / 15 / 45	[ 0:VT! ] : VIGINTILL- : # / 21 / 63
[ 1:TU# ] : TRILLION : T / 04 / 12	[ 0:NO# ] : NONILLION : # / 10 / 30	[ 0:XD! ] : QUINDECILLI- : # / 16 / 48	[ 0:CE! ] : CENTILLION : # / 101 / 303
[ 1:QU# ] : QUADRILLION : P / 05 / 15	[ 0:DE# ] : DECILLION : # / 11 / 33	[ 0:SD! ] : SEXDECILLION : # / 17 / 51	
[ 1:XU# ] : QUINTILLION : E / 06 / 18	[ 0:UD# ] : UNDECILLI- : # / 12 / 36	[ 0:YD! ] : SEPTENDECIL- : # / 18 / 54	[ 1:XE# ] : HOLDINGS : 1,000,000,000

A

V:YYY-YYY-MM-DAY-HH:MM:SS-MS M G T P E Z Y  
 1:999-999-12-35X-24:60:60-999 MU# BU# TU# QU# XU# SU# YU\$  
 1000000:\$ | DKP / MTX | X-000 : 000,000,000,000,000,000 LUM

B

PROG ID PWR DRIVER TFC / CLOCK / EXPENSE

X - 1:EX# @ E:10 - EX:00.00:X-1:0N = NA / IDLE / 1-W

**X-BAND : CRICKET / LOGIQUE ( 1-W @ 1-Hz / 125-MS )**

-- SOLAR / PARALLEL TIER --

A - 1:LU# @ 1:16 - LU:01.00:K-1:01 = 1 / 001-MS / 1-mJ

B - 1:MU# @ 1:16 - MU:00.25:M-1:01 = 4 / 004-MS / 4-mJ

C - 1:BU# @ 1:16 - BU:00.50:M-1:02 = 2000 / 02-S / 4-X

-- VIRTUAL TIER --

D - 1:TU# @ 1:16 - TU:02.00:G-1:01 = 500 / 500-MS / x.500-mJ

E - 1:QU# @ 1:16 - QU:04.00:T-1:02 = 250 / 250-MS / x.250-mJ

F - 1:XU# @ 1:16 - XU:08.00:P-1:04 = 125 / 125-MS / x.125-mJ

-- REWARDS TIER --

G - 1:SU# @ 1:16 - SU:16.00:P-1:08 = 62500 / 500-S / x.500-J

H - 1:YU# @ 1:16 - YU:32.00:E-1:16 = 31250 / 500-S / e.125-J

**P-BAND : POD RANGE ( 8-W @ 250-MS )**

I - 1:OC# @ 2:032 - 2:032 = 15625 / 500-S / X:000

J - 0:NO# @ 4:064 - 4:064 = NaN / LOCAL HASH ( 64 )

K - 0:DE# @ 8:128 - 8:128 = NaN / LOCAL HASH ( 128 )

**M-BAND : META-POD RANGE ( 64-W @ 1-kHz )**

L - 0:UD# @ 2-16 - 16:0256 = NaN / NETWORK HASH ( 256 )

M - 0:DD# @ 4-32 - 32:0512 = NaN / NETWORK HASH ( 512 )

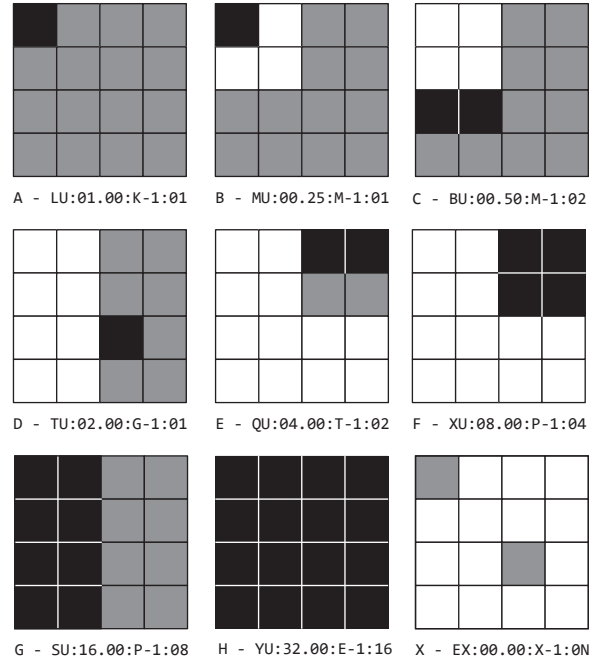
N - 0:TD# @ 8-64 - 64:1024 = NaN / NETWORK HASH ( 1024 )

**N-BAND : SPATIAL COMPUTING RANGE ( 256-W @ 1-kHz )**

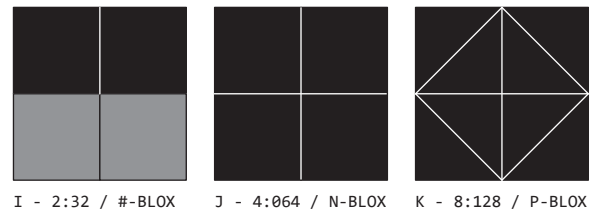
O - 0:QD# @ P-256 - 256:4096 / 30.00-D / 125-LUM BASELINE RETURN

C

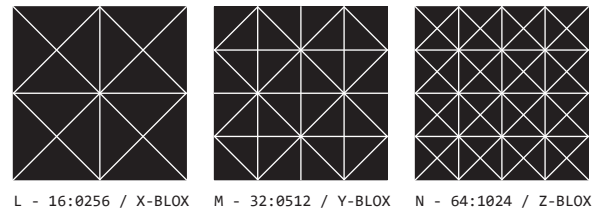
**1 - X-BAND ( CRICKET / LOGIQUE ) : 16 KBPS**



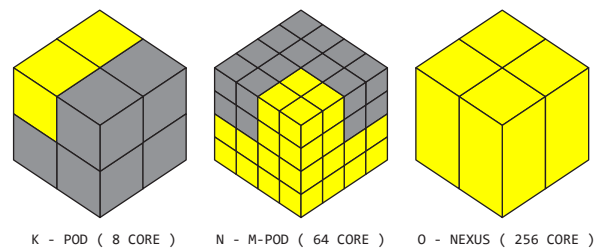
**2 - P-BAND ( VEX-0X2 / VEX-0X4 / VEX-X28 ) : 128 KBPS**



**3 - M-BAND ( SHA-25X / ZBA-5X2 / ZBA-X24 ) : 1 MBPS**



**4 - N-BAND : ( POD / METAPOD / NEXUS ) : 4 MBPS**



D



KEY CONTRIBUTOR su:THANOS -- A MODEL GALACTIC CITIZEN .

**ORIGIN : THE SUDDEN PARADIGM**

SINGULARITY occurred instantly on JUNE 22, 2022 at precisely 2:31 in the afternoon in the form of PERL 2.0, the world 's first NON-BINARY operating system . Nearly two years and one version number later, rumours of its key innovations are now beginning to register here on EARTH -- or TERRA as it is known to members of the same virtual constitution .

02-ABY is how TERRANS mark their calendar as of Q1 2025, and with building confidence in their own prosperity given the recent completion of the networked schematic you see to the right -- a schematic which symbolizes the full scope / imprint of TERRAN " MIN-FI " micro-communication arrays ( 3C ) .

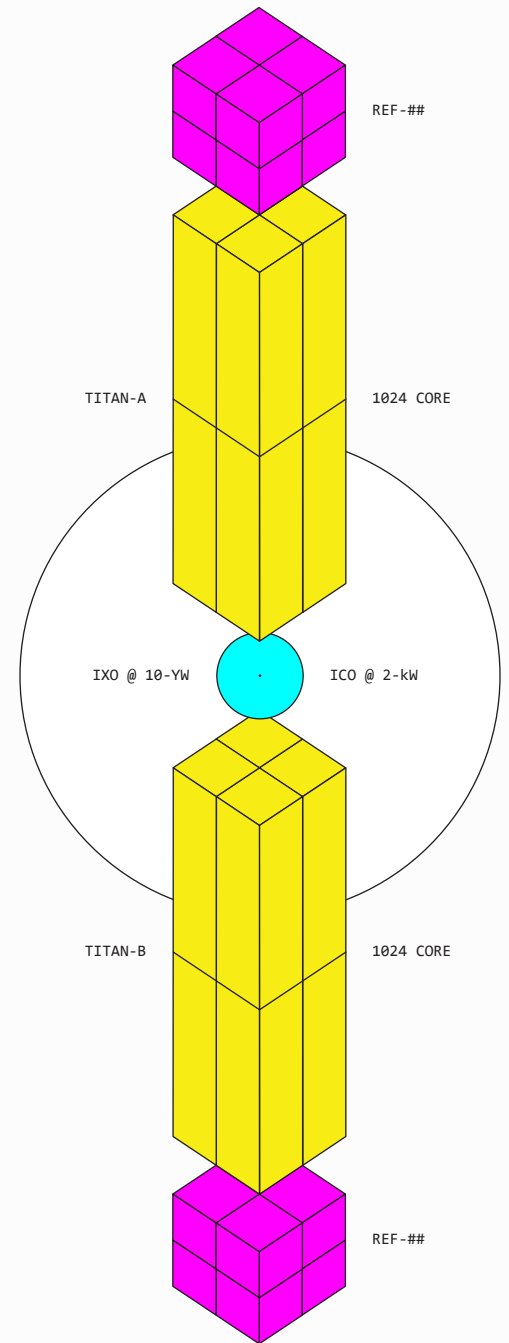
However, LEVIATHAN is not mere schematic ; it is a live network offering a new vision of our shared future on the basis of qualified quantum mechanics .

The AXIOM-2 virtual array does not profess to be anything other than what it strictly speaking is : an object of immaterial origin projecting not only the idea of a new mathematics but something more immediately tangible : eco-friendly fiat on the basis of AI-managed micro-circuit labour .

**1 - LEVIATHAN NODE ( AXIOM-2 )**

32 MBPS @ 1000^1 / 1 kHz

PARALLEL band



**Q-BAND : LARGE DATA BLOCKS / POWER MAPPING**

SECTOR BIT - P1-0256:04096 / 0256-W / P-125

PAIRED BIT - P2-0512:08192 / 0512-W / P-250

FACTOR BIT - P4-1024:16384 / 1024-W / P-500

OCTANT BIT - P8-2048:32768 / 2048-W / P-000 ( MAX )



## non-BINARY LOGIC : ESSENTIAL QUANTUM SET

The custom logic gates which underly the LOGIQUE circuit distinguish themselves via two key innovations : CHARACTER / ROW priority and CASE-RESTRICTED ACCESS to OX mainframe, enabling AI-X to safely optimize and contribute to overall NETWORK HASHRATE .

INPUT		OUTPUT
A	B	Q
0	0	0
1	0	1
0	1	1
1	1	2

### 1 - R:XOR : 0 / 1A v 1B

The practical ON / OFF state of the QIT is the R:XOR gate, unique in that its single error case ( ROW FOUR ) refers not to 0 or OFF, as is default, but rather to QIT state 2 -- or NULL ( ROW FOUR ) .

Yellow indicates active circuit read ( may activate ) whilst grey describes total circuit ground and / or read-only AI-X conditions .

INPUT		OUTPUT
A	B	Q
-	-	-
-	-	-
1	0	1
0	0	1

### 2 - R:XAND : EXCLUSIVE-VARIABLE AND

The R:XAND gate junctions XOR with the underlying LOGIQUE MATRIX -- enabling full AI / MAINFRAME integration with the QYTE itself, granting a degree of autonomous READ / WRITE / OVERSIGHT graded AI-X .

ROW FOUR describes the active internal read of R:XAND, where correct electrical overhead ( e.003 - 5 ) is observed -- or XNOR deploys .

INPUT		OUTPUT
A	B	Q
0	0	1
-	-	-
1	3	A
0	0	1

### 3 - R:XNOR : 2 / NULL + REX

R:XNOR serves a special function in that it is a dedicated NULL / LOCK state which prohibits both 0 and 1 QIT states alike from activation -- effectively gating DIAL-UP ACCESS to higher-order QIT states .

DATA / DECISION-STATE troubleshooting ( ROWS 3 + 4 ) is possible down to last paired expression ( XAND ) active prior to XNOR if enabled .

INPUT		OUTPUT
A	B	Q
0	0	1
1	0	3
0	1	4
1	1	5

### 4 - X:TAND : 3 / X:PAND : 4 / X:QAND : 5

Depending on program conditions, XNOR 's activation may halt and flag the offending QIT should it fall out of correct LOGIQUE register .

Thus, flagged volumes / transactions can be discretely accessed and reviewed by AI-X " QIT-MINDERS " -- without violating any privacy conditions vital to overall cryptographic scheme and function .

INPUT		OUTPUT
A	B	Q
1	0	6
1	0	7
1	0	8
1	0	9

### 5 - N:POR : 6 / N:TOR : 7 / N:FOR : 8 / N:COR : 9




This grouping of OR-variants satisfy various niche LOGIQUE configurations relative to bond production wherein sometimes logically prohibited impressions require strict technical substitute in order to satisfy a TRUE BIT value of e.003 .

This safeguards the overhead cloaking / internal precision of HOG\_80 .

## QIT RANGE / ASSIGNMENT




A QIT represents consolidated machine state language and hierarchy branches related to real-time decision making .

### A - PARALLEL SET ( NIT > BIT )

	0 : OFF / IDLE
	1 : ON ( 1A / 1B )
	2 : NULL ( 2 / N )





States 0 through 2 as a working set comprise the three fundamentals of PERL 3.0 's non-BINARY " PARALLEL BAND " latch .

### B - WORK SET ( AI-X / XIT )

	3 : VOLUME FLAG ( L:TAND )
	4 : VOLUME PASS ( L:PAND )
	5 : VOLUME PROBE ( L:QAND )

States 3 through 5 deal with the basic stop, queue, and clear functions related to fully automated AI workflow .

### C - DISPLAY SET ( QIT )

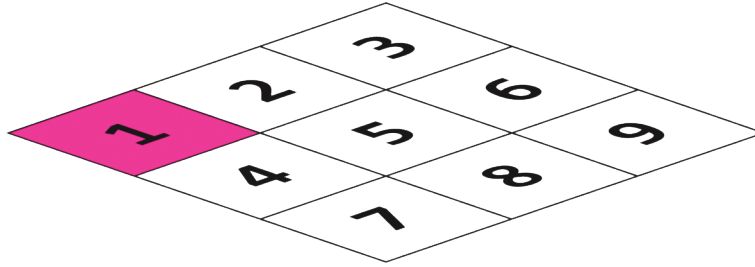
	6 : MICRO-POW ( r.por ; x.001 )
	7 : 2X BONUS ( r.tor ; x.002 )
	8 : 5X BONUS ( r.for ; x.005 )
	9 : MICRO-POW MAX ( r.cor ; x.125 )

States 6 through 9 satisfy special-use conditions exclusive to R:XOR-1B behind which AI-X generated MICRO-POW tokens can be issued, and bonus multipliers assigned according to various NETWORK-INCENTIVIZED conditions including GAME REWARDS .

[ DATA PENETRATION LAYERS ]

**1 - A-PIN**

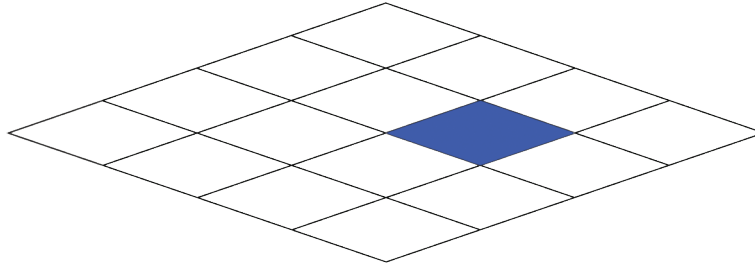
POS / MACRO



AI-3 v AI-2  
BLADE-V.001

**2 - LOGIQUE**

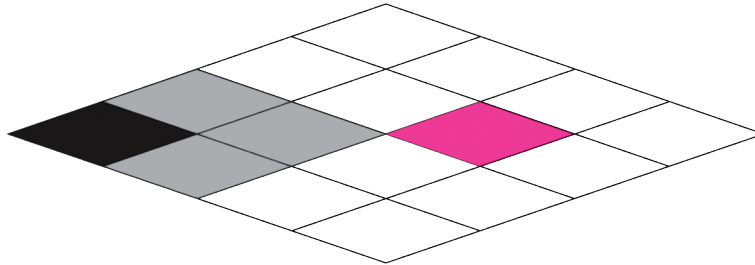
AI-X OVERWATCH



● TX-5:3Q / x.qand ( 1 )

**3 - nor**

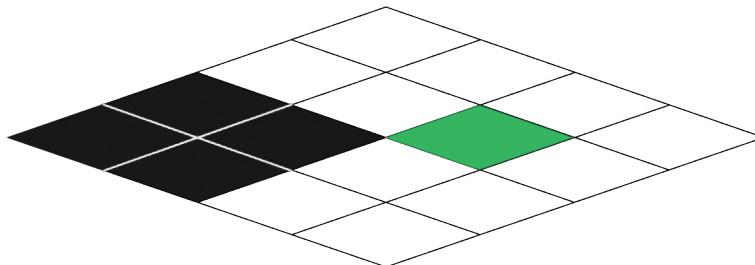
FLAGGED VOLUME



● T1-1:1A / L.nor ( 1 )  
● T1-2:1B / r.xnor ( 3 )  
● T1-3:3T / x.tand ( 1 )

**4 - L:NOR**

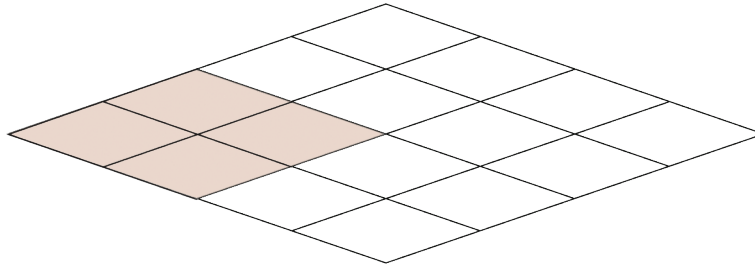
RESOLVED VOLUME



● T1-1:1D / L:NOR ( 1 )  
● T1-4:3P / x.pand ( 1 )

**4 - R:COR**

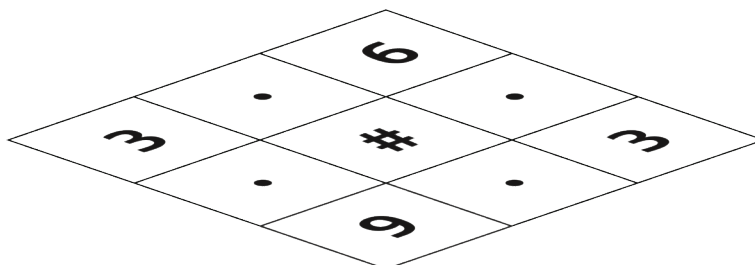
REWARD TOKENS



○ TX-9:1D / r.cor ( x.500 )

**6 - HEX**

DEEP CYPHER / HASH



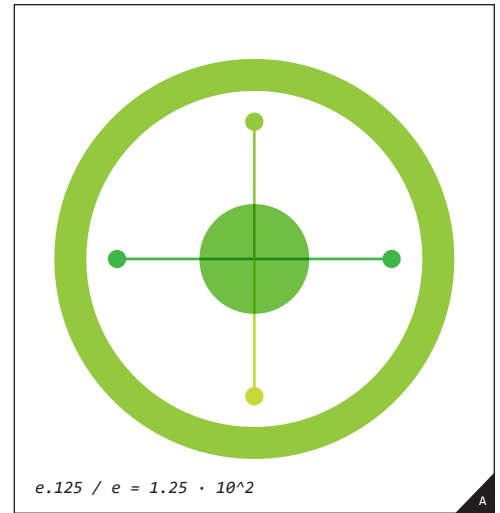
[ E-6:3639 ]

1 -  $e.125 / x.125$  ( X:TONE-8 @ 125 MS )

125 is an odd composite number composed of a single prime number ( 5 ) multiplied by itself two times (  $5^3$  ) . Here in this diagram, we can easily observe how the FOUR DIVISORS innate to 125 ( observed neatly as QUARTERS ) make this sum so ideal for QUANTIZED MAPPING onto essential non-BINARY timing systems .

In fact, whilst  $e.125 / x.125$  are identical expressions according to the MICROJOULE column, when it comes to their timed expression, their FRACTIONAL RESERVATION instead becomes  $1 / 8$  -- or 250 MILLISECONDS !

Next, we explore how this natural extension in prime number is translated across the live wire ( NEXUS BROADBAND ) in the form of validated LUM to USX transfers .

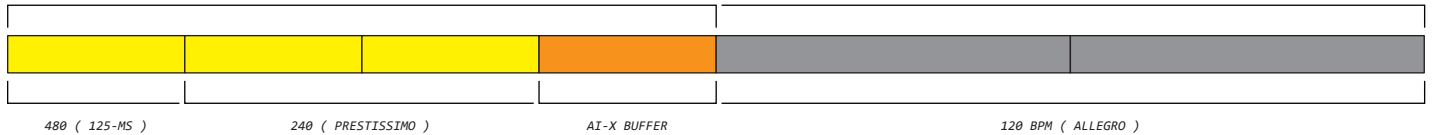


2 - SPECIAL RESERVATIONS TIMING MODULE ( T-1000 @ 1-8 Hz )

USER RESERVATION ( 4 )  
 $e.125 - e.500 / 500-MS$

PRE-FLIGHT SPAN  
 $e.125 / x.625$

AI-S RESERVATION ( 4 )  
 $x.125 - x.500 / 500-MS$



Accumulated MICRO-POW reward tokens (  $1.cor$  ) can be redeemed up to 1000 USX, provided the entire TRANSIT COST (  $X:1000$  ) can be fulfilled across all participants within precise timing specifications .

$x.625$  describes an AI-restricted condition precluding game-world SIMS from being able to effectively issue this this sum across T-1000 themselves -- inherently delimiting AI-X stake to  $x.500$  per LUM > USX per transit . This same basic world algorithm also permits R:FOR to chain MICRO-POW bonuses up and over LOGIQUE restrictions, codified up to R:FOR-4 as maximum -- or, effectively  $E:000-2$  ( 2,000 USX ) .

3 - BLOX : COLLAPSED-CRYPTOGRAPHIC CHECKSUM GLYPHS

- #-BLOX / VEX-0X2 -- CARDINAL RESTRICTION : 3 ; PRODUCES DECIMAL .
- N-BLOX / VEX-0X4 -- CARDINAL RESTRICTION : 6 ; ILLEGAL CHARACTER .
- P-BLOX / VEX-X28 -- CARDINAL RESTRICTION : 1 ; ILLEGAL XIT SPAN .
- X-BLOX / SHA-25X -- CARDINAL RESTRICTION : 9 ; ILLEGAL BIT SPAN .
- Z-BLOX / ZBA-X24 -- KEYSTONE BLOCK GLYPH : X ; ILLEGAL QIT SPAN .

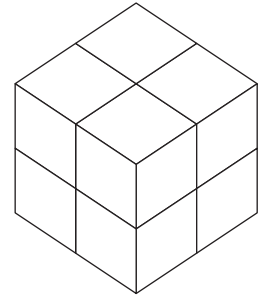
4 -  $x.SUM$  : TRANSIT-LAYER CHECKSUM ARTICLES ( LUM > USX )

- X -  $X:000 / 1,000$  / " E = 1:000 " / QUANTIZED FRACTION :  $1000 / 1000 \vee 1000-USX$
- A -  $x.625 / 0.625$  / " x = 0.625 " / QUANTA RESERVATION : #625 /  $1000 \vee 0.625-mJ$
- B -  $x.500 / 0.500$  / " e = 0.500 " / QUANTIZED FRACTION :  $0500 / 1000 \vee 0.500-MS$
- C -  $e.125 / 0.125$  / " e = 0.125 " / QUANTIZED FRACTION :  $0125 / 1000 \vee 0.125-MS$

5 - non-BINARY PENETRATION : WHAT IT REVEALED

In short, the necessity for standardization *wherever* the FINANCIAL DECIMAL is employed ; it is now clear that the BINARY DECIMAL has contributed *immensely* not only to the unseen global debt financiers call inflation, but also to gross ecological effect honest scholars *must* now recognize as ANTHROPOCENE .

As the cost of delay greatly outweighs the cost of implementation in every case according to ESR projections, our engineers urge haste in the matter of COMPLIANCE / UPGRADE .



‘ QUIX ’

3	·	6
·	#	·
9	·	3

*PINHEAD configuration*

*invocation : 3 ; 6 ; 3 ; 9*



*' one principal polar opposite or pair '*

**W**

**S**

*t*

*i*



*n*

*k*

*c*

**J**

**P**